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(( (COP\$4 OR REPLICAT\$4 OR DUPLICAT\$4) AND EXTRACT\$4 AND LOAD\$3 AND (SIMD OR SINGLE NEAR1 INSTRUCTION) AND (SHUFFL\$4 OR ALIGN\$7 OR ROTAT\$4 OR SHIFT\$4) NEAR5 (INSTRUCTION OR COMMAND OR MICROINSTRUCTION) AND (BYTE\$1 OR BIT\$1) AND (FIRST OR BEGINNING OR START\$4) ).CLM.).PGPB.	0

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<u>L31</u>	( extract\$4 and load\$3 and (simd or single near1 instruction) and (shuffl\$4 or align\$7 or rotat\$4 or shift\$4) near5 (instruction or command or microinstruction) and (byte\$1 or bit\$1) and (first or beginning or start\$4) ).clm.	3	<u>L31</u>	
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<u>L29</u>	((simd or single near1 instruction) and (shuffl\$4 or align\$7 or rotat\$4 or shift\$4) near5 (instruction or command or microinstruction) and (byte\$1 or bit\$1) and (first or beginning or start\$4) ).clm.	25	<u>L29</u>	
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<u>L28</u>	((simd or single near1 instruction) and (shuffl\$4 or align\$7 or rotat\$4 or shift\$4) near5 (instruction or command or microinstruction) and (byte\$1 or bit\$1) and (first or beginning or start\$4) ).clm.	61	<u>L28</u>	
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<u>L18</u>	L17 and 11	525	<u>L18</u>	
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	<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L16</u>	load\$4 near25 align\$7	38276	<u>L16</u>	
<u>L15</u>	l3 and 17	21	<u>L15</u>	
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<u>L1</u>	(simd or single near1 instruction) and (shuffl\$4 or align\$7 or rotat\$4 or shift\$4) near5 (instruction or command or microinstruction)	2679	<u>L1</u>

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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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1. **A Uniform Representation of Single-and Multistage Interconnection Networks Used in SIMD**  
Pradhan, D.K.; Kodandapani, K.L.;  
*Computers, IEEE Transactions on*  
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2. **Minimization of Interprocessor Communication for Parallel Computation**  
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*Computers, IEEE Transactions on*  
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